

Amended Under Art. 34

Filed 23 January, 2001

# CLAIMS

1. An image capturing apparatus comprising:

an image sensor provided with a chip having an image capturing area and a package to which the chip is attached, said image capturing area consisting of a plurality of blocks, each of the blocks having image information read-out lines;

an optical system for focusing incident light from an object to be captured on the image capturing area of the image sensor; and

a position adjustment mechanism for changing a relative position between the image capturing area of the image sensor and an optical axis of the incident light directed from the optical system to the image capturing area, by 1/2 a longitudinal and lateral length of the blocks.

2. An image capturing apparatus according to claim 1, wherein the position adjustment mechanism is capable of displacing the image sensor with respect to the optical

axis.

3. An image capturing apparatus according to claim 1, wherein the position adjustment mechanism is capable of displacing the optical system with respect to the image sensor.

4. An image capturing apparatus comprising:

an image sensor provided with a chip having an image capturing area and a package to which the chip is attached, said image capturing area consisting of a plurality of blocks, each of the block having an image information read-out lines;

an optical system for focusing incident light from an object to be captured on the image capturing area of the image sensor;

an image sensor mount section to which the image sensor is replaceably attached,

wherein a plurality of kinds of the packages are provided, and the packages include one to which the chip is attachable so that a center of whole of all the blocks constituting the image capturing area coincides with an optical axis of the incident light, and other one to which the chip is attachable so that a center of at least one of the blocks constituting the image capturing area coincides

with the optical axis of the incident light.

5. An image sensor comprising a chip, wherein an image capturing area of the chip comprises a plurality of blocks each having image information read-out lines, and voltage supply wires for controlling circuitry in each block is provided in a region along at least one of demarcation lines between the blocks.

6. An image sensor according to claim 5, wherein the chip is cuttable along another one of the demarcation lines between the blocks.

7. (Cancelled)